



Re the Application of:

RAJESH R. SHAH, ET AL.

Application No.: 09/450,381

Filed: November 29, 1999

For: A Cluster With Multiple Paths Between

**Hosts And I/O Controllers** 

Art Group: 2126

Examiner: Anya, Charles E.

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**Technology Center 2100** 

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF TAYLOR & ZAFMAN LLP

Date: September 13, 2004

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Annie G. Pearson

09-13-04

Date

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<i>'Hg</i> '	Application Number	09/450,381					
STATEMENT BY APPLICANT		Filing Date	November 29, 1999	DEOE			
		First Named Inventor	Rajesh R. Shah	RECE	IV		
(use as many sheets as necessary)				Art Unit	2126		
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Shee	<b>t</b> 1	of	2	Attorney Docket Number	42390P7719 -	ehnology Co	

	U.S. PATENT DOCUMENTS						
Examiner Cite No.1		Document Number	Publication Date		Pages, Columns, Lines, Where		
	Oile	Number - Kind Code² (if known)	or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Relevant Passages or Relevant Figures Appear		
	1	US-6,587,950 B1	07-01-2003	Shah et al.			
	2	US-6,591,309 B1	07-08-2003	Shah			
	3	US-6,738,818	05-18-2004	Shah et al.			
	4	US-6,766,470 B1	07-20-2004	Shah			
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FOREIGN PATENT DOCUMENTS						
Examiner Cite No.'	Cite	Foreign Patent Document			Pages, Columns, Lines,	
	Country Code³ - Number⁴ - Kind Code³ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T°	
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Signature		Considered	

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 'Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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## **INFORMATION DISCLOSURE** STATEMENT BY APPLICANT

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Complete if Known					
Application Number	09/450,381				
Filing Date	November 29, 1999				
First Named Inventor	Rajesh R. Shah				
Art Unit	2126				
Examiner Name	Anya, Charles E.				
Attorney Docket Number	42390P7719				

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	5	PCI Local Bus Specification, Production Version, Revision 2.1, June 1, 1995, 1992, 1993, 1995 PCI Special Interest Group.	
	6	Virtual Interface Architecture Specification, Draft Revision 1.0, December 4, 1997, 1997 Compaq Computer Corp., Intel Corporation, Microsoft Corporation.	
	7	Next Generation I/O Link Architecture Specification: Fabric Service Interface Specification, Realease 1.0.0: July 20, 1999.	
	8	Next Generation I/O Link Architecture Specification: HCA Specification, Release 1.0.0: July 20, 1999.	
	9	Next Generation I/O Link Architecture Specification: Link Specification, Release 1.0.0: July 20, 1999.	
	10	Next Generation I/O: Principles of Operation, Release 1.0.0: July 20, 1999.	
	11	Next Generation I/O Link Architecture: Switch Specification, Release 1.0.0: July 20, 1999.	
	12	Next Generation I/O Link Architecture Specification: Channel Interface Specification, Release 1.0.0: July 20, 1999.	

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Signature	Considered	

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